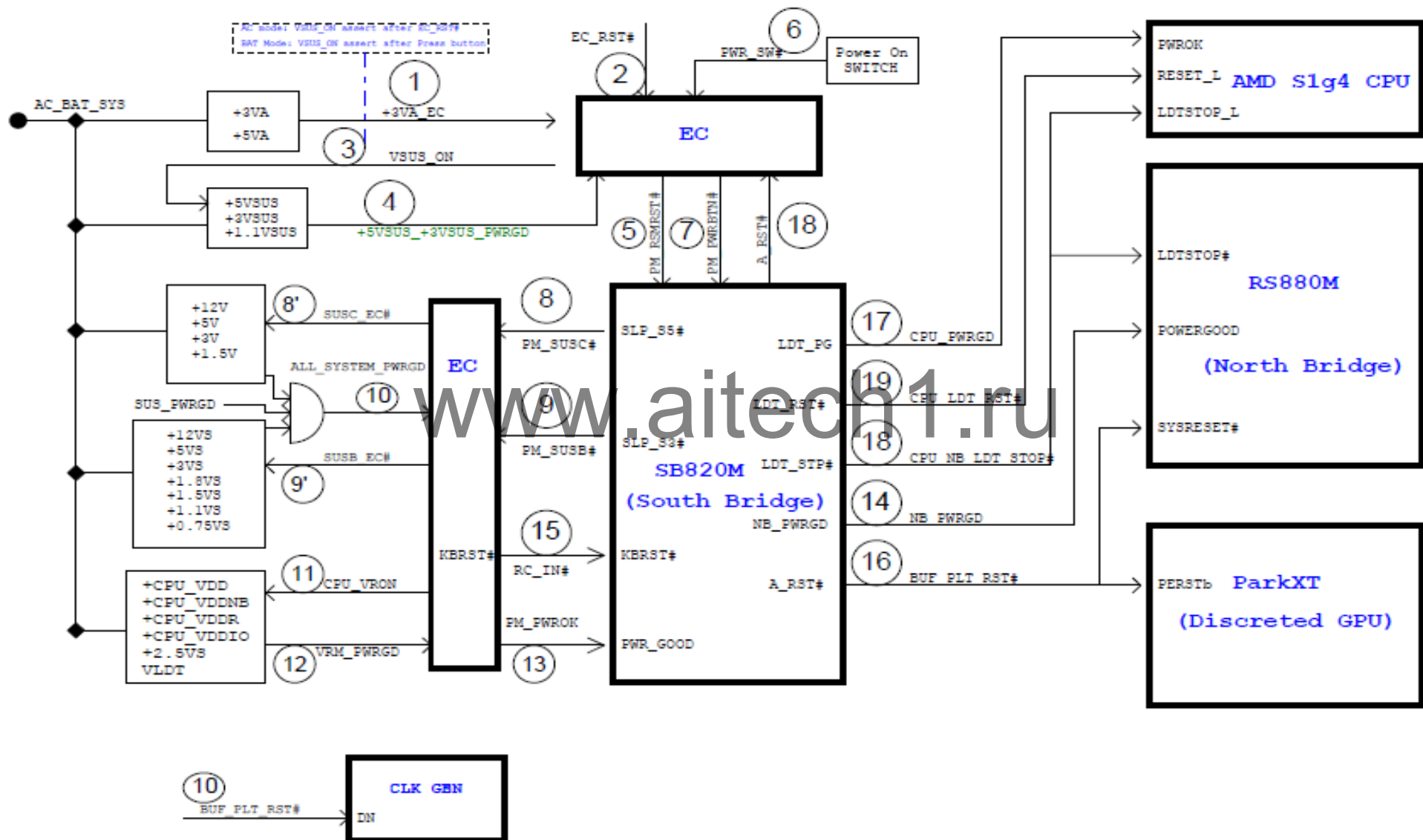


The diagram illustrates the system architecture for the AMD S1G4. At the top, the **AMD CPU S1G4** is connected to two **DDR3 SO-DIMM** modules via **DDR3** interfaces. The CPU is connected to the **RS880M** chipset through an **HT 3.0 2.0GHZ, dual** link. The **RS880M** chipset is connected to the **SB820M** chipset via an **X4 A-Link Express II** interface. The **RS880M** also manages **PCI-E** connections, including a **x1 PCI-E** to the **MiniCard Conn. 802.11 b/g/n**, **GLAN**, **USB3.0**, and **EXPRESS CARD**. The **SB820M** chipset is connected to the **EC ITE8500** via an **LPC** interface. The **EC ITE8500** manages the **KB/TouchPad**, **SPIROM**, and **Debug Conn.**. The **SB820M** also manages **SATA** (SATA HDD, SATA ODD, ESATA) and **USB** (USB 2.0 X2, USB 2.0 X1, CardReader) connections. The **SB820M** is connected to the **Codec Realtek ALC269** via an **HD AUDIO** interface, which is then connected to the **Audio Jack**. A large watermark **www.aitech1.ru** is overlaid on the diagram.

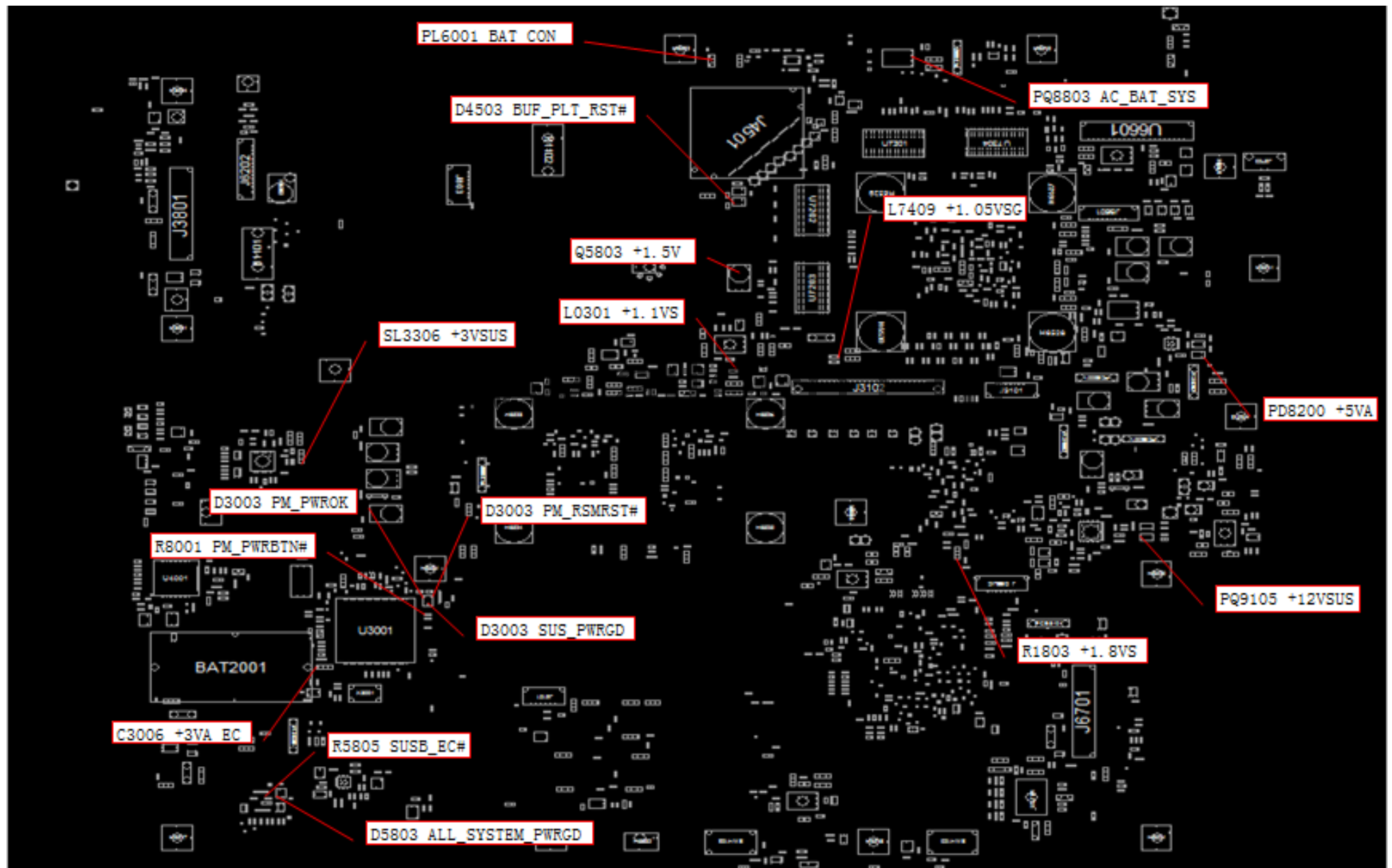
POWER FLOW



POWER ON SEQUENCE



Voltage & Signal Measure Point TOP



Voltage & Signal Measure Point Bottom

